# Development of Eco-Friendly Ag Embedded Peroxo Titanium Complex Solution Based Thin Film and Electrical Behaviors of Resistive Random Access Memory

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Abstract In this study, we introduce a novel TiN/Ag embedded TiO<sub>2</sub>/FTO resistive random-access memory (RRAM) device. This distinctive device was fabricated using an environmentally sustainable, solution-based thin film manufacturing process. Utilizing the peroxo titanium complex (PTC) method, we successfully incorporated Ag precursors into the device architecture, markedly enhancing its performance. This innovative approach effectively mitigates the random filament formation typically observed in RRAM devices, and leverages the seed effect to guide filament growth. As a result, the device demonstrates switching behavior at substantially reduced voltage and current levels, heralding a new era of low-power RRAM operation. The changes occurring within the insulator depending on Ag contents were confirmed by X-ray photoelectron spectroscopy (XPS) analysis. Additionally, we confirmed the correlation between Ag and oxygen vacancies (V<sub>o</sub>). The current-voltage (*I-V*) curves obtained suggest that as the Ag content increases there is a change in the operating mechanism, from the space charge limited conduction (SCLC) model to ionic conduction mechanisms. Further, we propose a novel filament model based on changes in filament configuration and the change in conduction mechanisms. Further, we propose a novel filament model that encapsulates this shift in conduction behavior. This model illustrates how introducing Ag alters the filament configuration within the device, leading to a more efficient and controlled resistive switching process.

Key words resistive random-access memory, peroxo titanium complex, resistive switching, Ag embedded TiO<sub>2</sub>, conduction mechanism.

# 1. Introduction

The advancement of technologies such as big data and artificial intelligence (A.I) necessitates faster digital information processing compared to previous times.<sup>1)</sup> However, scaling down existing silicon-based semiconductor devices has limitations in achieving the required high-speed processing capability. Therefore, much research attention has been focused on next-generation memory devices.<sup>2)</sup> Among these devices, resistive random access memory (RRAM) has been extensively studied owing to its simple structure, fast operation speed, low power consumption, and excellent scalability of MIM (metal-insulator-metal).<sup>3-7)</sup> The functionality of RRAM relies on the resistive state change of the insulator. The conduction filament is formed through the SET process, where the insulator transitions from a high resistance state (HRS) to a low resistance state (LRS). The filament is ruptured through a RESET operation, which involves a change from LRS to HRS.

Resistance change properties have been observed in binary transition metal oxides such as TiO<sub>2</sub>, NiO, and ZnO,<sup>8-10)</sup> as well as in complex oxides like PrCaMnO (PCMO) and Ba<sub>x</sub>Sr<sub>1-x</sub>TiO<sub>3</sub>.<sup>11,12)</sup> Among materials with such characteristics, TiO<sub>2</sub> stands out as a binary transition metal oxide due to its

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high density, simple structure, and fast switching properties.<sup>13)</sup> Therefore, TiO<sub>2</sub> has been widely investigated as a resistive switching (RS) layer in RRAM devices.<sup>14,15)</sup> The fabrication of TiO<sub>2</sub> thin films can be achieved through vacuum-based or solution-based deposition methods. Vacuum-based methods such as sputter, atomic layer deposition (ALD), and chemical vapor deposition (CVD) have been primarily utilized because of their excellent thickness control and uniform film formation.<sup>16)</sup> However, these processes have limitations in terms of cost and complexity.<sup>17)</sup> On the other hand, solution-based methods, such as spin coating and hydrothermal methods, offer low-cost, simple processes.<sup>2,18)</sup> Also, this process has the potential for fabrication of transparent thin films. Among these processes, spin coating has been extensively researched as a method for manufacturing RRAM devices due to its ability to produce thin films and ease of process control.<sup>19)</sup> To utilize the advantageous spin coating method, it is necessary to fabricate TiO<sub>2</sub> as a solution-based material. Therefore, in a previous study, we synthesized TiO<sub>2</sub> using the peroxo titanium complex (PTC)-ion method, which is ecofriendly and differs from the conventional TiO<sub>2</sub> fabrication methods.<sup>20-22)</sup> Moreover, we successfully fabricated a TiN/ TiO<sub>2</sub>/FTO structure RRAM device using PTC-ion solution and demonstrated successful resistance change properties.<sup>23)</sup> The insulator layer of the RRAM device was fabricated using a PTC-ion solution derived from eco-friendly materials, specifically TiO<sub>2</sub>. Unlike other solution-based TiO<sub>2</sub> fabrication methods that utilize organo-metal or metal precursors, our PTC-ion synthesis method is safe for human and eco-friendly. However, while a ~45 nm thin film was successfully produced via spin coating, the resulting film's uniformity was inferior to that obtained through vacuum deposition processes. Typically, uniformity and thinness are crucial for achieving low power consumption. Moreover, non-uniform films can lead to the formation of random filaments. Previous research optimized the spin coating process to produce a uniform film, but the issue of random filament formation still persists.

In order to regulate the formation of random filaments in RRAM devices, various studies have been conducted, such as the use of multi-layers or thin metal layers.<sup>24-27)</sup> Nevertheless, the presence of dipoles caused by heterojunctions in multi-layer structures can impede carrier movement, resulting in an increase in the forming voltage.<sup>28)</sup> Therefore, chan-

ges in device characteristics due to the introduction of precursor materials into TiO<sub>2</sub> thin films have been reported.<sup>29,30)</sup> Moreover, since the process is solution-based, the precursor material can be introduced into the TiO<sub>2</sub> thin film by simply adding it to the PTC solution. The insertion of metal ions is expected to influence the performance of RRAM devices by affecting filament formation. Various materials, such as Al and Ag, which are used as top electrodes in conductivebridge random access memory (CBRAM) devices, are potential metal precursors. Among these, Ag can be promising candidate for the insertion of metal precursor. Due to Ag's high mobility characteristics, it is widely used as an upper electrode in RRAM devices and has recently gained attention in the field of neuromorphic computing.<sup>31,32)</sup> However, as with excessive ion doping, excessive insertion of precursors can lead to deteriorated cycle characteristics, decreased resistance change, and reduced structural stability. Hence, identifying the optimal ratio is crucial.

In general, RRAM devices using TiO<sub>2</sub> as an insulator layer exhibit behavior characteristic of oxygen vacancy resistive access memory (OxRAM), which involves conduction through the formation and rupture of filaments containing oxygen vacancies  $(V_0)$ .<sup>33)</sup> On the other hand, CBRAM relies on a mechanism that involves the formation and rupture of conductive filaments through cations such as Ag or Cu that drift from metal electrodes.<sup>34)</sup> In the case of an RRAM device fabricated by inserting a metal precursor into the TiO<sub>2</sub> layer, it is expected that a conduction mechanism involving both metallic ions and oxygen vacancies will arise, which is distinct from the general case. Since most insulator layers are fabricated by vacuum deposition, research on directly inserting metal precursors into the insulator layer has not been conducted much yet. Thus, it is crucial to comprehend the fabrication mechanism to effectively apply RRAM devices which inserted metal precursor.

In this study, we aimed to fabricate a TiN/Ag embedded TiO<sub>2</sub>/FTO RRAM device through a simple process of embedding Ag nanoparticles in a PTC solution. By optimizing the composition and processing temperature, we successfully confirmed the change in resistance switching characteristics. To investigate the effect of the Ag concentration on resistance change characteristics, we fabricated devices with AgNO<sub>3</sub> concentrations of 0.3, 0.5, and 0.7 at%. We then analyzed the current-voltage (*I-V*) characteristics of the Ag

embedded RRAMs and identified the appropriate concentration of AgNO<sub>3</sub> precursor. Furthermore, by elucidating the new conduction mechanism, we were able to reduce the operating voltage through the conductive properties of Ag particles. Ultimately, we developed a reliable, eco-friendly, and low-temperature solution process based on PTC-ion.

# 2. Experimental Procedure

#### 2.1. Device preparation

The PTC-sol was prepared using a jacketed beaker in the same manner as in our previous study while maintaining the reaction temperature below 50 °C, titanium hydride (IV) (TiH<sub>2</sub>, 99 %, Sigma-Aldrich) was added to a mixed solution of hydrogen peroxide (H<sub>2</sub>O<sub>2</sub>, 30 %, DAEJUNG) and ammonium hydroxide (NH<sub>4</sub>OH, 25~30 %, DAEJUNG), after that followed by stirring for 1 h to stabilize the solution.<sup>23)</sup> An Ag-PTC-ion solution containing silver nitrate (AgNO<sub>3</sub>, 99 %, Sigma-Aldrich) ions of 0.3 at%, 0.5 at%, and 0.7 at% compared to Ti was prepared. The prepared PTC ion solution at room temperature was heated at 80 °C for 10 min and then cooled to a temperature of 15 °C or less again. Finally, it was completely dissolved to prepare a transparent Ag-PTC-ion solution. Fluorine doped tin oxide (FTO) glass surface to be used as an electrode was cleaned in order to prepare a uniform TiO<sub>2</sub> thin film. 0.25 mL of the prepared Ag-PTC solution was dripped onto FTO glass through spin coating for 3,000 rpm, 30 s, 3 cycles, and then heat-treated at 100 °C to prepare a TiO<sub>2</sub> thin film. Afterward, a titanium nitride (TiN) electrode having a diameter of 0.2 mm and a height of 100 nm was deposited by RF magnetron sputtering through a circular shadow mask.

## 2.2. Resistive switching test

The I-V curve measurements were conducted in DC

voltage sweep mode using a Keithley 4200A-SCS semiconductor parameter analyzer. For the TiN/TiO<sub>2</sub>/FTO devices, DC bias voltages were applied incrementally at 0.05 V/step across a range of  $0 \pm 8$  V. In instances where the FTO bottom electrode was grounded in the TiN/Ag embedded TiO<sub>2</sub>/FTO device, the DC bias was applied to the TiN top electrode at a step of 0.05 V within a sweep range of  $0 \pm 5$  V, and a compliance current (C.C) of 0.1 A was implemented. The retention characteristics were verified through retention time measurements, while cycle durability was assessed by analyzing cycle characteristics. Furthermore, the *I-V* curves were subjected to linear fitting analysis during both positive and negative voltage sweeps to evaluate the device's performance relative to the Ag content.

## 3. Results and Discussion

# 3.1. Physical and chemical characteristics

As shown in Fig. 1, it can be confirmed that a  $TiO_2$  thin film is deposited on the pristine FTO glass after 3 cycles of spin coating. As is well known, the thickness and uniformity of the RS layer affect the operating voltage characteristics of the device. So, a thin and uniform film is important for low voltage characteristics. As a result of further enlargement through TEM, it was confirmed that a dense TiO<sub>2</sub> layer was deposited in the range of 50 to 100 nm. In general, PTC-ion solutions have low viscosity and high shrinkage, so it is difficult to prepare a uniform thin film. Through the process used in our previous study, a sufficiently uniform thin film was produced compared to other vacuum deposition processes.<sup>23)</sup> To determine the optimal RRAM device, X-ray photoelectron spectroscopy (XPS) analysis was performed to obtain information on the elements and chemical states of the samples. Fig. 2 displays the high-resolution XPS peaks of Ti (2p), O (1s), and Ag (3d) for the (a) pristine  $TiO_2$ , (b) 0.3 at%



Fig. 1. SEM images of TiO<sub>2</sub> thin films by cycle (a) Pristine-FTO (b) 3 cycles TiO<sub>2</sub> coated FTO, TEM images (c) of 3 cycles TiO<sub>2</sub> coated FTO.

embedded TiO<sub>2</sub> and (c) 0.5 at% embedded TiO<sub>2</sub> RRAM devices. The Ti (2p<sub>3/2</sub>) peaks were respectively 458.17 eV and 456.86 eV in (a). Also, 0.3 at% Ag embedded TiO<sub>2</sub> RRAM device in (b) show characteristic peaks at 458.18 eV and 456.83 eV, which correspond to Ti<sup>4+</sup> and Ti<sup>3+</sup>, respectively.<sup>23)</sup> Similarly, according to (c), the 0.5 at% Ag embedded TiO<sub>2</sub> RRAM device appear at 458.18 eV and 456.98 eV. It was confirmed that TiO<sub>2</sub> was successfully formed in all samples. Compared to the (a), intensity of Ti was decreased by the addition of Ag. It goes with the formation of Ti<sup>3+</sup> also decreased. Ti<sup>3+</sup> is generated by the formation of oxygen vacancies, and it acts as a defect.<sup>35)</sup> However, it shows that the ratio of Ti<sup>3+</sup> in (c) is increased compared to (b). This seems to affect oxygen vacancies by increasing the formation. The

presence of many oxygen vacancies can lead to a decrease in operating voltage. However, as SET/RESET switching is repeated, cycle stability may be adversely affected due to the reduction of oxygen ions. Additionally, the defect site inside can act as an electron trap site and can have an electrical effect on the device. The O (1s) peak exhibits characteristic peaks at 529.67 eV, 531.33 eV, which correspond to Ti-O, -OH respectively. In (b), the binding energy of O (1s) are 529.64 eV, 530.73 eV, 532.07 eV, and 532.4 eV, which correspond to Ti-O, Ag-O, -OH, and O<sub>2</sub>.<sup>36,37)</sup> Similarly, respectively bondings were characterized at 529.7 eV, 530.79 eV, 532.13 eV, and 532.52 eV in (c). In (b) and (c), the presence of Ag-O peaks was confirmed following the insertion of Ag. A notable change was observed in the -OH peak. Unlike the RRAM devices in the (a), both samples showed a



**Fig. 2.** Ti (2p), O (1s), Ag (3d) XPS spectra of TiO<sub>2</sub> thin film (a) Pristine TiO<sub>2</sub> RRAM, (b) 0.3 at% embedded RRAM device, (c) 0.5 at% Ag embedded RRAM device.

significant increase in the area of the -OH peak, which seems to have resulted from the interaction between Ag and TiO<sub>2</sub>. The amount of Ag-O bonding slightly increased with the addition of Ag. Unlike (a), Ag  $(3d_{5/2})$  spectra of (b) and (c) were characterized at 367.67 eV and 367.62 eV, respectively. Each sample is divided into 367.5 eV, 368.01 eV, and 367.53 eV, 368.13 eV, which correspond to Ag<sup>+</sup> and Ag<sup>0</sup>, respectively.<sup>38)</sup> An increase in intensity was confirmed with the addition of Ag. Additionally, Ag exists more as Ag<sup>+</sup> than Ag<sup>0</sup>, which corresponds to the Ag-O peak in the O (1s) peak. The binding energy of bulk Ag (3d<sub>5/2</sub>) was assigned at 368.3 eV, and both samples were shifted to the lower position.<sup>39)</sup> These results are likely due to the strong interaction of electrons moving from TiO<sub>2</sub> to metallic Ag.<sup>37)</sup>

#### 3.2. Resistive switching characteristics

Based on the results of previous studies, the basic characteristics of the Ag embedded TiO<sub>2</sub> RRAM devices were investigated by applying a voltage sweep range of  $0 \pm 8$  V with a consistent C.C value of 0.1 A.<sup>23)</sup> Fig. 3 illustrates the *I-V* curve under the condition of 0.3 at% Ag embedded  $TiO_2$ RRAM. Initially, during the positive voltage sweep, the current level gradually increased, leading to a SET operation at +3.7 V. This was followed by maintaining the LRS during the reverse voltage sweep, where the RESET operation occurred at -3.7 V. The investigated I-V curve results showed a variance from those of conventional OxRRAM. Firstly, compared to OxRRAM, the initial resistance value was increased by four orders of magnitude, resulting in a low initial resistance value of 10<sup>-6</sup>. Furthermore, it was observed that the SET/RESET voltage significantly decreased to -3.7 V/ 3.7 V according to the voltage sweep range. This indicates that the addition of Ag altered the resistance characteristics of the TiO<sub>2</sub> layer, and the material forming filaments shifted



**Fig. 3.**  $0 \pm 8$  V voltage sweep range, 0.3 at% Ag embedded TiO<sub>2</sub> RRAM *I-V* characteristics under 0.1A C.C condition.

to metallic filaments, exhibiting characteristics of CBRAM. As the SET/RESET voltage range decreased, sweeping at unnecessarily high voltage ranges could stress the device. Therefore, the voltage sweep range was reduced to  $\pm 5$  V, and simultaneously, the C.C was decreased by one order of magnitude to apply a current of 10 mA. Under these modified conditions, Fig. 3 characterizes the *I-V* curves of Ag embedded TiO<sub>2</sub> RRAM with varying Ag content.

Electrical analysis was conducted on TiN/Ag-embedded TiO<sub>2</sub>/FTO RRAM devices with varying Ag content (0.3 at%, 0.5 at%, and 0.7 at%), to investigate the effect of Ag concentration on resistance switching characteristics. Fig. 4(a) illustrates the *I-V* curve of the RRAM device with 0.3 at% Ag, indicating typical nonvolatile bipolar RS behavior. During the negative voltage sweep, the current gradually increases, and a SET process (transition from HRS to LRS) occurs due to a rapid rise in current level at  $V_{set}$ : ~-3 V. On the other hand, during the positive voltage sweep, LRS is maintained, and a RESET process (transition from LRS to HRS) occurs through a sudden drop in current at  $V_{reset}$ : ~3 V. The TiN/0.3 at% Ag-embedded TiO<sub>2</sub>/FTO RRAM device showed successful RS characteristics, reaching the initial resistance value



**Fig. 4.** Switching behavior (SET, RESET) of TiN/Ag-embedded TiO<sub>2</sub>/FTO RRAM device according to compliance current and voltage sweep range (a) 0.3 at%, (b) 0.5 at%, (c) 0.7 at%.

of 10-8. Similarly, the RRAM device embedded with 0.5 at% Ag also showed RS behavior, as shown in Fig. 4(b), with a gradual rise in current during the negative voltage sweep, and a SET process at V<sub>set</sub>: ~-2 V. During the positive voltage sweep, LRS is maintained, and a RESET process occurs at Vreset: ~2.4 V. Therefore, the TiN/0.5 at% Ag-embedded TiO<sub>2</sub>/FTO RRAM device also showed successful RS characteristics, reaching an initial resistance value of 10<sup>-8</sup>. However, when the Ag content was 0.7 at%, complete ohmic conduction characteristics were observed, with the absence of SET and RESET processes. The initial resistance of the TiN/0.7 at% Ag embedded TiO<sub>2</sub>/FTO RRAM device was greatly reduced, with an initial resistance value of 10<sup>-6</sup>, unlike the 0.3 at% and 0.5 at% embedded RRAM devices. Hence, at high Ag concentrations, it appears that the resistance change layer does not function as an insulator. While the RRAM devices with 0.3 at% and 0.5 at% Ag were successfully behaved as RRAM devices, the 0.7 at% embedded RRAM device did not. As the content of Ag increased from 0.3 at% to 0.5 at%, the V<sub>set</sub> and V<sub>reset</sub> of the RRAM device decreased, likely due to an increase in the proportion of Ag ions forming filaments. Furthermore, the Vset and Vreset of the RRAM device with 0.3 at% and 0.5 at% embedded Ag were significantly reduced compared to the TiN/TiO<sub>2</sub>/FTO RRAM device fabricated in a previous study, with a reduction of about 4 V or more each.<sup>23)</sup> Despite having the same thickness, the values of Vset and Vreset decreased by more than half depending on the Ag content. In general, the resistance change characteristic in binary oxide-based devices is caused by the formation and rupture of filaments by oxygen ions.<sup>40</sup> However, in the case of TiO<sub>2</sub> layers containing Ag, a change in the conduction mechanism is expected to occur due to the formation of a conduction bridge by the movement of Ag ions.

Fig. 5 illustrates the cycle characteristics of the RRAM device according to content of Ag. As Ag is inserted, cycle performance is improved. In Fig. 5(a), resistance change characteristics no longer appear after 4 cycles. This is because  $O^{2-}$  ions generated by cycle repetition in the RS layer made of pristine  $TiO_2$  combine with each other and lose as  $O^2$ . However, in the case of Fig. 5(b), the number of escaped  $O^{2-}$  is reduced due to the presence of Ag, resulting in improved stability. And it shows that the resistance change characteristics were consistently maintained up to 10 cycles. The insertion of Ag ostensibly enhances cycle performance. On the other hand, the RRAM device containing 0.5 at% Ag embedded TiO<sub>2</sub> in Fig. 5(c) could not maintain the resistance change characteristics as the cycle number increased, and the device showed ohmic conduction behavior after only 5 cycles. While low Ag content improves device stability by reducing the loss of O<sup>2-</sup> ions, leading to enhanced cycle performance, a higher Ag content (0.5 at%) introduces challenges. The RRAM device with 0.5 at% Ag exhibits ohmic conduction behavior after a limited number of cycles due to the incomplete oxidation of Ag filaments. Similar to the behavior of a typical CBRAM device, when an opposite voltage is applied to the device, the Ag filament is oxidized to Ag<sup>+, 41)</sup> The excessive Ag leads to a build-up of unoxidized Ag atoms, adversely affecting the device's resistive switching characteristics.<sup>42)</sup> Therefore, the application of the RRAM device containing 0.5 at% Ag as a reliable memory device is limited. In addition to cycle performance, the retention time is an essential factor in evaluating the performance of a memory device.43)

The retention characteristics of the pristine  $TiO_2$  and 0.3 at% Ag-embedded  $TiO_2$  RRAM device are presented in Fig. 6. Both devices were shown a stable current level in both HRS and LRS for a period of 3,600 s, which is consistent



**Fig. 5.** Cycle characteristics under (a) Pristine TiO<sub>2</sub> RRAM, (b) 0.3 at% Ag embedded TiO<sub>2</sub> RRAM and (c) 0.5 at% Ag embedded TiO<sub>2</sub> RRAM.

with the retention behavior observed in typical RRAM devices.<sup>42)</sup> Additionally, the ON/OFF ratio for the pristine device (a) was measured at  $10^4$ , and for (b), it was found to be greater than  $10^3$ , aligning with the values derived from the *I-V* curve. For a non-volatile memory to be useful in practical applications, the ON/OFF ratio value should typically be at least 10 times higher than the reported value.<sup>44-46)</sup> Therefore, the observed ON/OFF ratio value of 3 orders of magnitude or greater confirms the suitability of the implemented device for use as a non-volatile memory. In addition, considering the above cycle stability, it was confirmed that Ag was inserted at 0.3 at% as optimal.

## 3.3. Resistive switching mechanism

Previous measurements of the current-voltage characteristics have indicated that the TiN/Ag embedded TiO<sub>2</sub>/FTO device operates through filament formation and migration of Ag ions as well as filament formation by oxygen vacancy. However, to fully comprehend the dominant mechanism of the TiN/Ag-embedded TiO<sub>2</sub>/FTO device for its practical application, it is necessary to confirm the conduction mechanism. Therefore, we analyzed the conduction mechanism of 0.3 and 0.5 at% Ag samples, where resistance change characteristics were observed. Fig. 6 show the I-V curve and double- logarithmic plot of TiN/Ag-embedded TiO<sub>2</sub>/FTO devices with 0.3 at% and 0.5 at% Ag content. The conduction mechanism was confirmed by performing linear fitting of I-V curves during positive and negative voltage sweeps. As shown in Fig.7(a), for the 0.3 at% Ag sample, the slope at high voltage during the negative bias sweep was found to be 2 or more, and a sudden increase in current was observed, confirming the dominance of the SCLC mechanism. The

linear fitting of the 0.3 at% Ag sample also showed a slope of 1.4 during the negative bias sweep at high voltage. Typically, a slope close to 1 indicates either no current flow due to high resistance or ohmic conduction resulting from internal thermal carriers. However, since the slope was found to be higher than 1, the conduction mechanism can be attributed to Schottky emission  $(\log(I) \propto V^{0.5})$  during the low voltage range of 0~1 V in the HRS state. The direction and shape of filament formation by Ag ions are determined by the mobility of cations and the reduction rate of ions in the dielectric.<sup>37)</sup> In the case of the TiN/Ag embedded TiO<sub>2</sub>/FTO device, Ag ions move to the TiN electrode at a negative bias to form a tip, as the mobility of Ag ions is higher than the reduction rate. Therefore, the gap between the upper and lower electrodes is reduced due to the formed tip, resulting in a reduction in resistance and facilitating filament formation, leading to conduction by Schottky emission.

Subsequently, at voltages between 2 to 3.1 V, the 0.3 at% Ag sample exhibits a slope of 3.1, accompanied by a sharp increase in current, which is dominated by the trap-limited space charge limited current (SCLC) conduction mechanism  $(I \propto V^2)$ . At this point, the injected carriers conduct a more significant role in determining the conduction characteristics due to an increase in carrier injection as compared to thermally generated carriers in the thin film. This phenomenon arises from the formation of a conductive path through the filament, which has been previously reported in Ti/Al<sub>2</sub>O<sub>3</sub>/ AgNPs/Al<sub>2</sub>O<sub>3</sub>/p-Si structures, where Ag atoms and oxygen vacancies form filaments together.<sup>41)</sup> The conductivity is substantially enhanced during this period, leading to the SET process. Oxygen vacancies ( $V_o^+$ ) migrate towards TiN in response to the negative voltage applied to the top electrode,



Fig. 6. Retention time characteristics under (a) Pristine TiO<sub>2</sub> RRAM, (b) 0.3 at% Ag embedded TiO<sub>2</sub> RRAM.

forming filaments that follow the previously formed Ag ion tips. Additionally, Ag particles act as seeds in the conductive filament, reducing the randomness of filament formation and decreasing the conductivity path of the filament.<sup>47)</sup> In the following -1 V to 0 V sweep range, the slope of the 0.3 at% Ag sample is 1.2, indicating ohmic contact  $(I \propto V)$  conduction. Similarly, during the 0 V to 0.9 V sweep range, which corresponds to a positive bias, the slope is 0.96, confirming the ohmic contact  $(I \propto V)$  characteristic.<sup>48)</sup> This indicates that the positive bias applied to the top electrode is insufficient to break the hybrid filament composed of Ag atoms and oxygen vacancies. After that, the RESET voltage is reached, and the slope up to  $\sim 2$  V is 4.9, demonstrating the trap-limited SCLC conduction mechanism. The slope during the 2 V to 0 V range is 1.7, following the Schottky emission mechanism. In the case of the 0.5 at% Ag sample, the result of the linear fitting in Fig.7(b) shows that the slope at high voltage has a value of 2 or more during the sweep to negative bias, accompanied by a sudden increase in current, confirming the dominance of the SCLC mechanism. However, the linear fitting result of the RRAM device containing 0.5 at% Ag, as shown in Fig.7(b), exhibits a high slope. As the Ag content increases, the amount of Ag that forms the filament increases, exhibiting characteristics that are consistent with the ionic conduction model. This outcome indicates that, unlike the 0.3 at% embedded RRAM, Ag becomes the primary component constituting the filament. The slope of the ionic conduction model can be calculated as follows.<sup>49)</sup>

$$\operatorname{Ln} J \propto \operatorname{Ln} V - \left(\operatorname{Ln} T + \frac{d}{T}\right) \tag{1}$$

The current density J, applied voltage V, temperature T, and a constant d are the parameters involved in the calculation of the slope in Fig. 7(b). Specifically, the HRS in Fig. 7(b) exhibited a slope of 1.6, while the slope increased to 4.1 in the LRS after the SET voltage was applied. The LRS slope was maintained at 1.7. When the RESET voltage was applied, a high slope of 4.8 was observed, although no current jumping was detected. This behavior is typical of CBRAM, where the metallic ion forming the filament is reduced to the top electrode during RESET operation and disappears within the insulator. However, even after reduction, Ag ions remain inside the insulator in Ag-inserted devices. As demonstrated



Fig. 7. I-V curve of Ag embedded TiO<sub>2</sub> RRAM (a) 0.3 at% Ag embedded RRAM and (b) 0.5 at% Ag embedded RRAM.

by the previous cycle characteristics, excessive Ag content inside the insulator reduces device resistance and degrades RRAM performance, such as the ON/OFF ratio. Therefore, these results indicate that the electrical behavior and conduction mechanism in the RRAM device are affected by the Ag content, and the optimal performance is achieved when 0.3 at% of Ag is incorporated.

Based on the results described above, we present a model of filament formation through the movement of Ag ions and oxygen vacancies, as illustrated in Fig. 8. In the pristine state, before any voltage is applied, Ag is present in the TiO<sub>2</sub> layer in the form of Ag<sup>+</sup> and Ag atoms.<sup>28)</sup> TiO<sub>2</sub> generally exists in a non-stoichiometric form, TiO<sub>2-x</sub>, where x represents a nonstoichiometric amount.<sup>50,51)</sup> The presence of non-stoichiometric amounts is typically caused by gaps in titanium or oxygen vacancies, with the latter resulting from insufficient oxygen supply during the deposition of TiO2.52) As such, oxygen vacancies are already present in the TiO<sub>2</sub> layer even before the application of any voltage. When a negative voltage is applied, Ag ions in the TiO<sub>2</sub> layer move towards the TiN electrode, while Ag atoms are oxidized and also move towards the TiN electrode in the form of Ag<sup>+</sup>. The moved Ag ions are reduced to form Ag atoms, which then form tips.<sup>53,54</sup> Meanwhile, O<sup>2-</sup> ions move towards the positively biased lower electrode, where they are converted into O<sub>2</sub> molecules. As a result, numerous oxygen vacancies are formed at the lower electrode interface.<sup>55)</sup> As the negative bias becomes stronger, the formation of oxygen vacancies increases from the FTO direction (positive bias), leading to the formation of filaments. At the same time, Ag filaments are formed from the TiN direction, which is the opposite direction. The two filaments eventually connect, leading to the SET process. When a positive bias is applied to the upper electrode, the joule heat generated by the thermal effect causes the oxygen vacancy and Ag atom filaments to be cut, leading to the RESET process. However, due to the lower activation energy of oxygen vacancies, the breakdown of the filament occurs faster than that of Ag atom filaments.<sup>47)</sup> Additionally, most of the positive bias applied to the upper part of the two positive charge types, oxygen vacancies, is pushed to the opposite side, resulting in the rupture of the filament. In contrast, in the case of Ag atom filaments, an electrochemical process involving overvoltage is required for Ag, which is an atom, to return to ions. As a result, the rupture of the filament mainly occurs in oxygen vacancy filaments, while Ag filaments remain almost unchanged.

## 4. Conclusion

In this study, we confirmed the electrical properties of TiN/Ag-embedded TiO<sub>2</sub>/FTO RRAM devices fabricated using a eco-friendly PTC-ion solution-based process with AgNO<sub>3</sub> precursor added in varying concentrations. Comparing the *I-V* curves of devices with varying AgNO<sub>3</sub> precursor concentrations reveals that RRAM devices with 0.3 at% and 0.5 at% Ag content display resistive switching (RS) characteristics, whereas devices with 0.7 at% Ag do not. The device with 0.5 at% Ag shows lower switching voltages than the one with 0.3 at% but has a reduced difference in current levels between high and low resistance states and lower initial resistance. Additionally, the 0.3 at% Ag device retains its switching characteristics for over 10 cycles, outperforming the 0.5 at% Ag device, which fails after 5 cycles.

Therefore, it was confrimed that the addition of Ag to the  $TiN/TiO_2/FTO$  RRAM device can effectively prevent random filament formation and produce uniform RRAM devices with desirable electrical characteristics. By analyzing the *I-V* curves of different Ag content samples, it was found that 0.5



Fig. 8. A model of resistive switching mechanism in TiN/Ag embedded TiO<sub>2</sub>/FTO memory device.

at% Ag content provided only suitable I-V characteristics, while 0.3 at% Ag content showed suitable I-V curve, ON/ OFF ratio and long retention times. The conduction mechanism of the each RRAM device was also analyzed, showing that the electrical behavior and conduction mechanism varied with the Ag content. Through optimization of the Ag content, this study successfully overcame the limitations of the TiN/TiO<sub>2</sub>/FTO RRAM device, reducing SET/RESET voltage and C.C current, and improving cycle characteristics. In conclusion, our research presents a thorough analysis of cycle performance and retention characteristics under varying Ag contents, providing a comprehensive understanding of the necessary trade-offs and optimization strategies for developing high-density, low-power RRAM applications. This paves the way for its application in the advanced fabrication processes of RRAM devices, signifying a substantial step forward in the field.

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# **Declaration of Competing Interest**

The authors declare that they have no known competing financial interests or personal relationships that could have appeared to influence the work reported in this paper.

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